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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 10/101,950 03/21/2002 PAT 6,642,581

** FOREIGN APPLICATIONS *****

JAPAN 2001-095899 03/29/2001

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Met after Allowance Examiner's Signature <i>Shawn Magee</i> Initials <i>SM</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 1
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TITLE

Semiconductor device comprising buried channel region and method for manufacturing the same

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